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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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**Complete if Known**

|                               |                      |
|-------------------------------|----------------------|
| <b>Application Number</b>     | 10/761,047           |
| <b>Filing Date</b>            | 01/20/2004           |
| <b>First Named Inventor</b>   | Stephen R. Van Doren |
| <b>Art Unit</b>               | 2457                 |
| <b>Examiner Name</b>          | Burgess, Barbara N.  |
| <b>Attorney Docket Number</b> | 200313631-1          |

Sheet 1 of 1

**NON PATENT LITERATURE DOCUMENTS**

| Examiner Initials* | Cite No. <sup>1</sup> | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T <sup>2</sup> |
|--------------------|-----------------------|---|----------------|
|                    |                       | MARTIN, MILO M.K., ET AL., "Token Coherence: Decoupling Performance And Correctness", ISCH-30, pages 1-12, June 9-11, 2003  |                |
|                    |                       | RAJEEV, JOSHI ET AL., "Checking Cache-Coherence Protocols With TLA+", Kluwer Academic Publishers, 2003, pages 1-8   |                |
|                    |                       | HANDY, "The Cache Memory Book", 1998, Academic Press second edition, pages 144-155  |                |
|                    |                       | CHEN ET AL., "Write Caching in Distributed Filed Systems", Distributed Computing Systems, 1995., Proceedings Of The 15th Int'l Con., 30 May-2 June 1995, Pages 457-466  |                |
|                    |                       | ARCHIBALD ET AL., "Cache Coherence Portocols: Evaluation Using A Multiprocessor Simulation Model", November 1986, ACM Transactions on Computer Systems, Pages 273-298   |                |
|                    |                       | GCHARACHORLOO, KOUROSH ET AL., "Architecture and Design of AlphaServer GS320", Western Research Laboratory, (Date Unknown)  |                |
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|                    |                       | MARTIN ET AL., "Band width Adaptive Snooping", Proceedings Of The Eighth International Symposium On High-Performance Computer Architecture (HPCA'02), 2002 IEEE   |                |
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|                    |                   |                 |            |
|--------------------|-------------------|-----------------|------------|
| Examiner Signature | /Barbara Burgess/ | Date Considered | 08/21/2009 |
|--------------------|-------------------|-----------------|------------|

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /B.B./